



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Here application of: **DeWitt, Jr. et al.**

Serial No.: **10/757,227**

Filed: **January 14, 2004**

For: **Method and Apparatus for  
Autonomically Moving Cache Entries  
to Dedicated Storage when False  
Cache Line Sharing is Detected**

**35525**

PATENT TRADEMARK OFFICE  
CUSTOMER NUMBER

§ Group Art Unit: **2188**  
§  
§ Examiner: **Padmanabhan, Mano**  
§  
§ Attorney Docket No.: **AUS920030555US1**  
§

Certificate of Mailing Under 37 C.F.R. § 1.8(a)

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Commissioner for Patents  
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- Supplemental Information Disclosure Statement;
- Form PTO-1449;
- References AH-AJ; and
- Our return postcard.

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Respectfully submitted,

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of:

**DeWitt, Jr. et al.**

Serial No: **10/757,227**

Filed: **January 14, 2004**

Title: **Method and Apparatus for  
Autonomically Moving Cache Entries to  
Dedicated Storage When False Cache Line  
Sharing is Detected**

Group Art Unit: **2188**

Intellectual Property  
Law Department  
International Business  
Machines Corporation  
11400 Burnet Road  
Austin, Texas 78758

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicants submit herewith patents, publications or other information of which they are aware, which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 C.F.R. 1.56.

While this Supplemental Information Disclosure Statement may be "material" pursuant to 37 C.F.R. 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

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Respectfully submitted,

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Substitute for form 1449/PTO

**Complete if Known**

Application Number	10/757,227
Filing Date	01-14-2004
First Named Inventor	DeWitt, Jr. et al.
Examiner Name	Padmanabhan, Mano
Art Unit	2188
Attorney Docket No.	AUS920030555US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
AA	5,710,881	01-20-1998	Gupta et al.	
AB	5,822,763	10-13-1998	Baylor et al.	
AC	5,928,334	07-27-1999	Mandyam et al.	
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AG	2004/0205302 A1	10-14-2004	Cantrill	

## FOREIGN PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS				
Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

## NON-PATENT LITERATURE

NON-PATENT LITERATURE	
Examiner Initial	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
AH	TANENBAUM, "Structured Computer Organization", 1984, Prentice-Hall, Inc., 2 <sup>nd</sup> Edition, pp. 10-12.
AI	TORRELLAS et al., "False Sharing and Spatial Locality in Multiprocessor Caches", June 1994, IEEE Transactions on Computers, Vol. 43, No. 6, pp. 651-663.
AJ	ROTHMAN et al., "Analysis of Shared Memory Misses and Reference Patterns", 2000, IEEE, pp. 187-198.

Examiner:

Date Considered:

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.